TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOS VII)

TK10A60D

Switching Regulator Applications

• Low drain-source ON-resistance: $R_{DS (ON)} = 0.58 \Omega (typ.)$

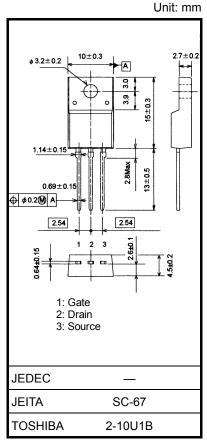
• High forward transfer admittance: |Y_{fs}| = 6.0 S (typ.)

• Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 600 V)$

• Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	600	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	I _D	10	Α
	Pulse (Note 1)	I _{DP}	40	A
Drain power dissipation	on (Tc = 25°C)	P_{D}	45	W
Single pulse avalanche energy (Note 2)		E _{AS}	363	mJ
Avalanche current		I _{AR}	10	Α
Repetitive avalanche energy (Note 3)		E _{AR}	4.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

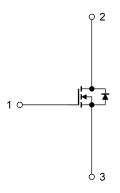
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W



Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 6.36 mH, $R_G = 25 \Omega$, $I_{AR} = 10 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2008-04

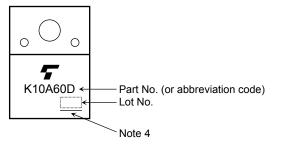
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rent	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current		I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	10	μΑ
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600			٧
Gate threshold ve	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0		4.0	٧
Drain-source ON	-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 5 A		0.58	0.75	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	1.5	6.0		S
Input capacitance		C _{iss}			1350		
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		6		pF
Output capacitance		Coss			135	_	
Switching time	Rise time	t _r	V_{GS} V_{OD}	_	22	_	
	Turn-on time	t _{on}		_	55	_	20
	Fall time	t _f			15	_	ns ns
	Turn-off time	t _{off}		_	100	_	
Total gate charge		Qg		_	25	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	_	16	_	nC
Gate-drain charge		Q _{gd}			9	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_			40	Α
Forward voltage (diode)	V_{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.7	٧
Reverse recovery time	t _{rr}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$		1300		ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs		12		μС

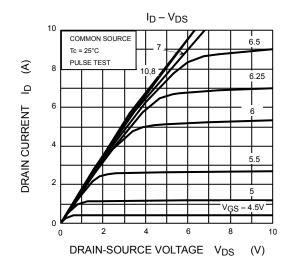
Marking

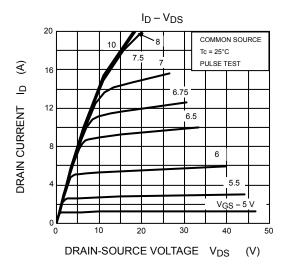


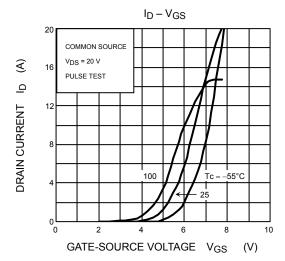
Note 4: A line under a Lot No. identifies the indication of product Labels.

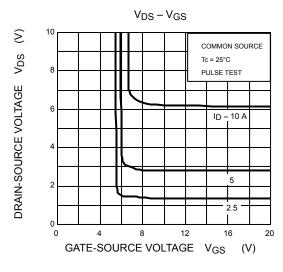
[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

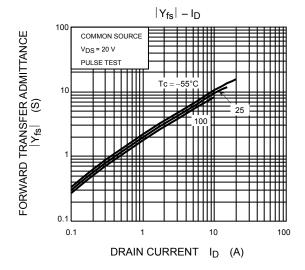
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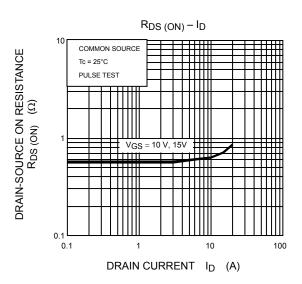


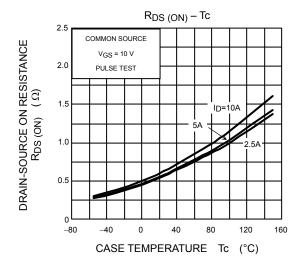


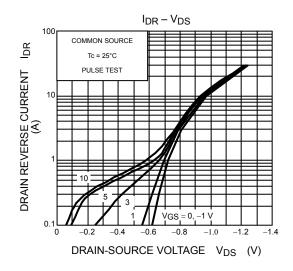


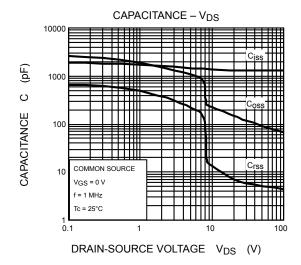


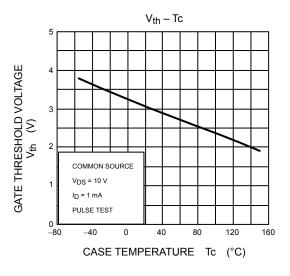


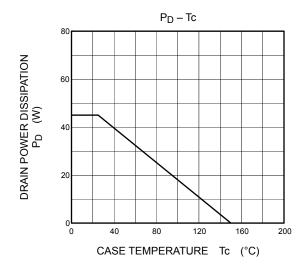


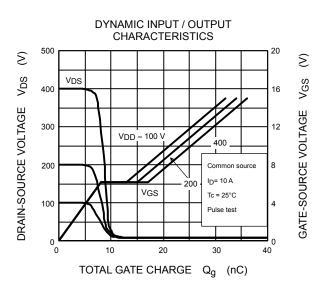


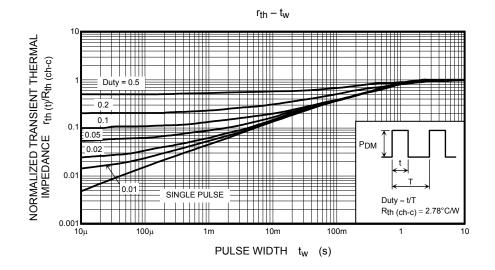


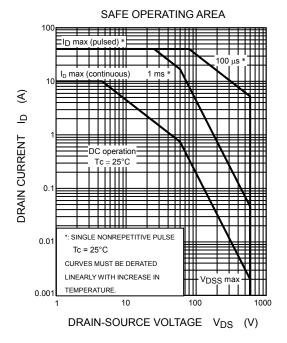


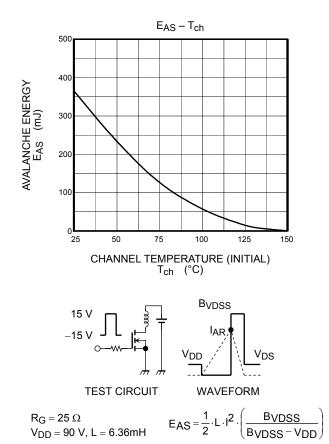












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